

GPS Timing In Substations at Dominion Energy

CIGRE USNC Grid of the Future October 23, 2017

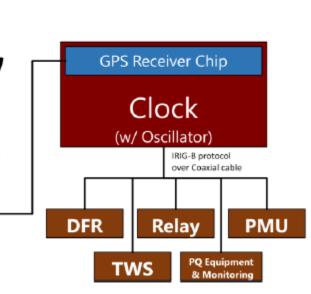
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History of our substation timing

- Since the early to mid 90s, Dominion has been using substation clocks to synchronize monitoring equipment.
- The first clocks used were GOES satellite and WWVB receivers.
- Began the transition to GPS clocks in the late 90s as costs lowered.

Current substation timing architecture

- A single GPS satellite clock installed at all Dominion transmission substations (100kV and above) and many distribution substations.
 - Located inside substation control houses, these are stand-alone GPS clocks, which have an internal GPS receiver chip
- Time synchronization is provided to many substation devices from the GPS clock via IRIG-B over coaxial cable
 - Includes Digital Fault Recorders (DFRs), protective relays, Phasor Measurement Units (PMUs), Traveling Wave fault locators (TWS), meters, and more.

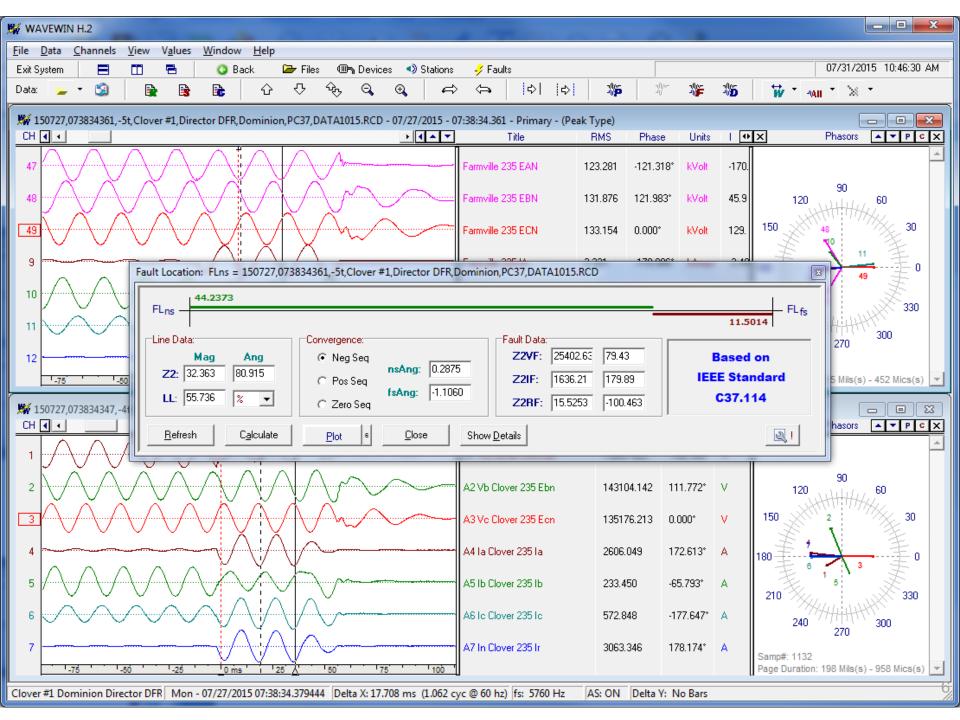


Current substation timing

- Until recently, substation timing has been taken for granted – it hasn't been as carefully scrutinized and tested as other equipment.
- Requirements & expectations are changing with the advent of new devices and expansion of substation digital devices that are able to make use of, or are dependent on, precise timing.
 - Starting to see more issues and alarms than previously

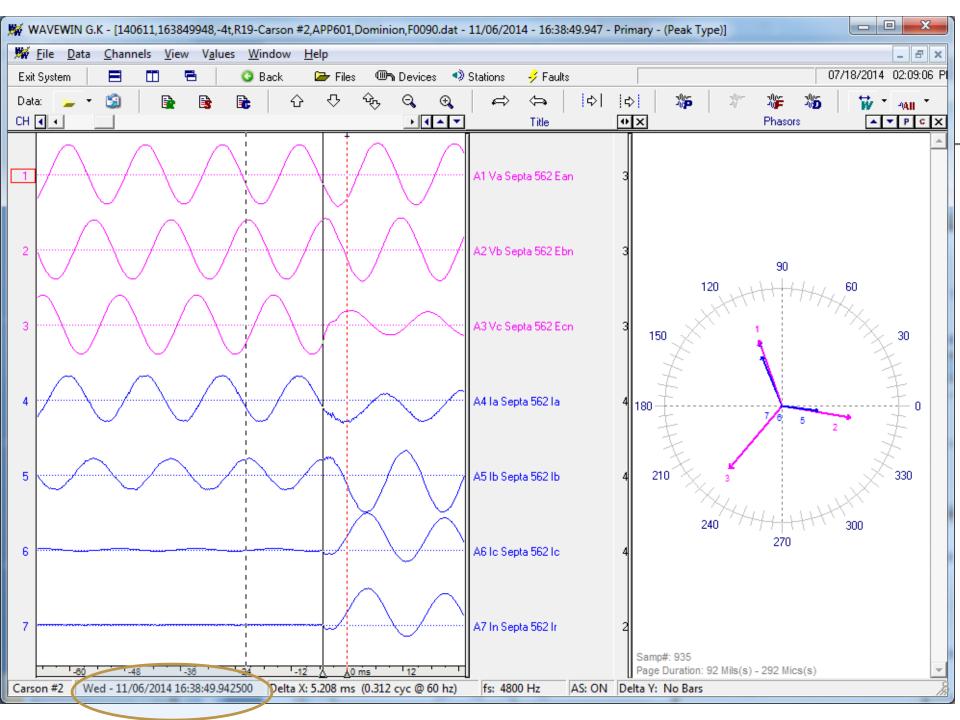
Timing Applications - DFRs

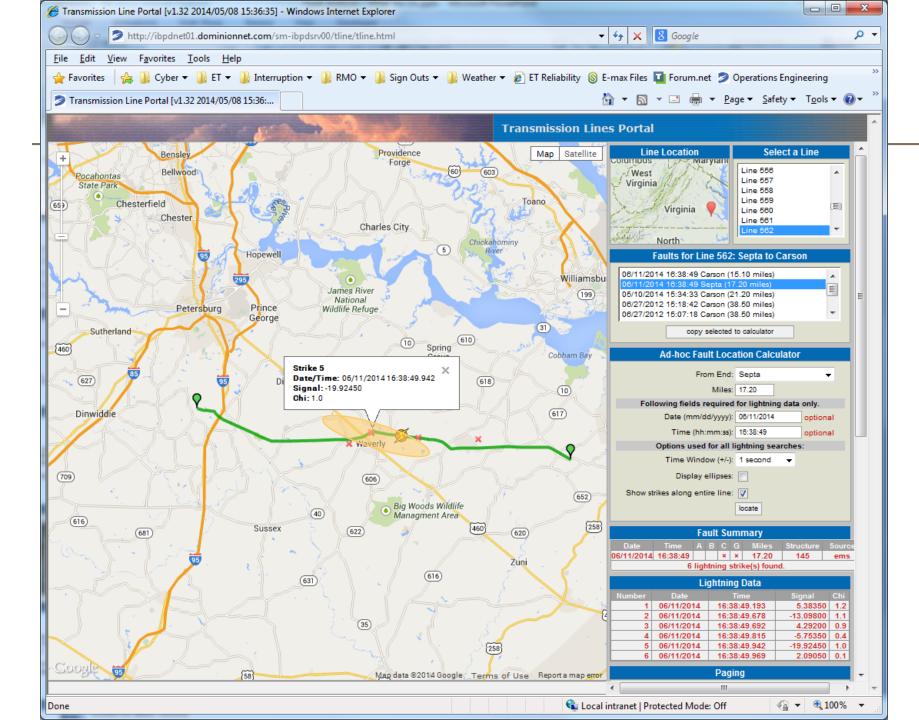
 Digital fault recorders (DFRs) (1 ms) - monitors the power flowing on the grid and take high sample rate snapshots of the currents and voltages. Time synchronized data can be matched with other recorders to analyze system events.



Timing Applications - Lightning

 Lightning correlation (1 ms) – Oscillography data can be used to determine fault inception time, which can then be used to determine if lightning caused the event by comparing our recorded event time to lightning strike data from a lightning service, such as FALLS from Vaisala.

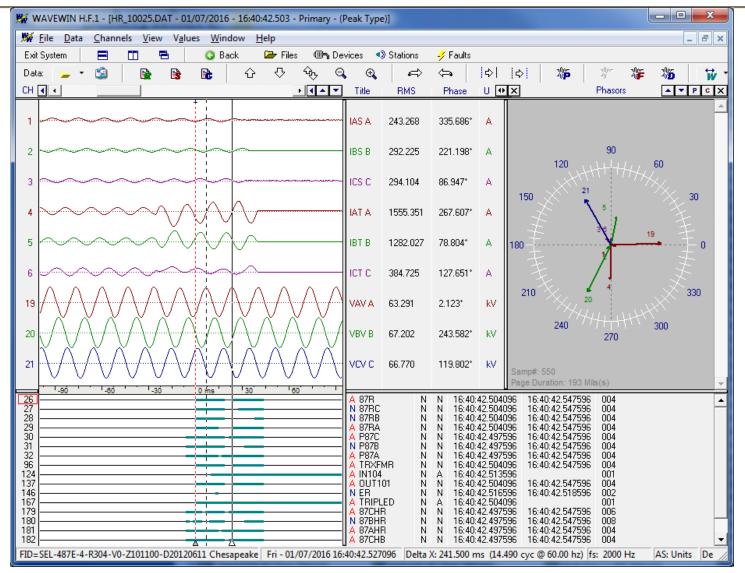




Timing Applications – Protective relays

- Protective relays (1 ms) similar to DFR in use of timing.
 - Provide time stamping of waveform data and internal logic events (SER records).

Timing Applications – Protective relays

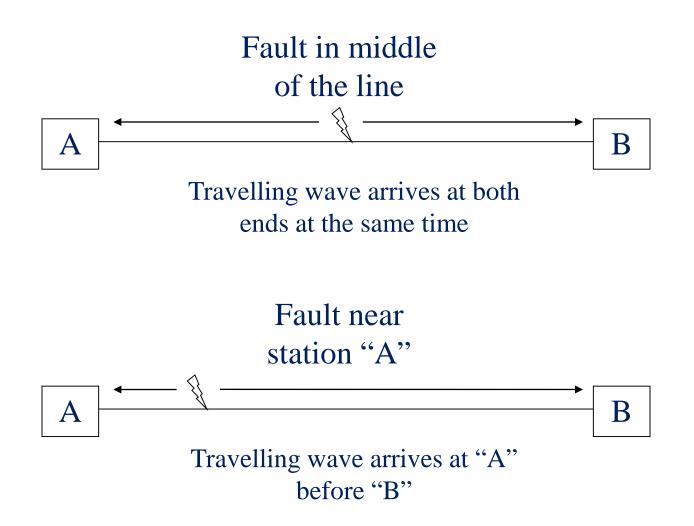


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Timing Applications - TWS

- Traveling wave fault location (0.1 µs) Uses precise time to measure high frequency waves generated by faults on transmission lines.
 - Wavefront arrival time is used to calculate fault location to within 500 feet or less.

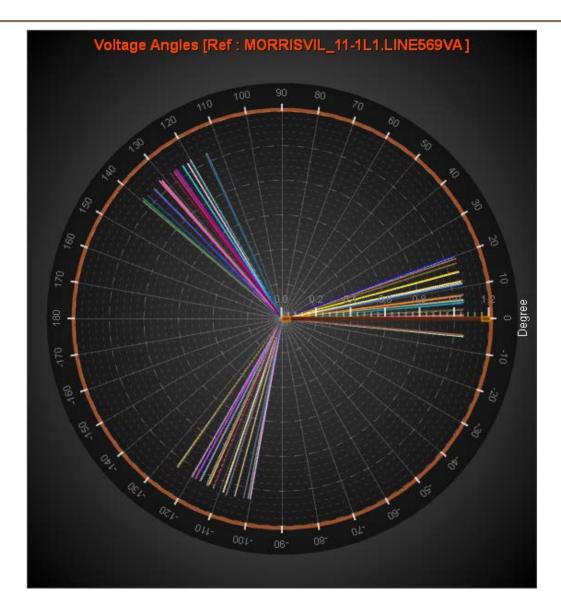
Travelling Wave Theory



Timing Applications - Synchrophasors

- Synchrophasors (1 µs)
- Synchrophasors are precisely time stamped voltage or current vectors.
- Measurements are taken simultaneously from across the grid.
- Operators can determine the health of the system from these measurements.

Synchrophasor angles



Timing Applications – PQ monitors

- Power quality monitors (1 ms) mostly on distribution level voltages, 34.5kV and below. Timing is used to correlate events recorded at different locations.
 - Utility and customer recordings.
 - Distribution events caused by transmission events.

- Future devices to receive time sync: Power line Carrier sets
 - Recent event with PLC issues. No time sync on the PLC SER led to difficult and extended event analysis and RCA.

Timing Problems

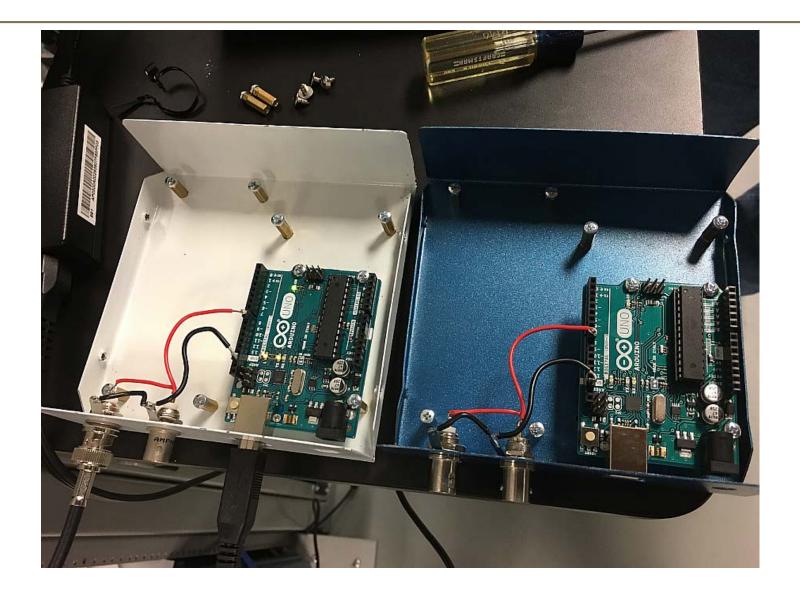
Timing events

- Problems surrounding the changeover during Daylight Savings Time transitions
- Leap second handling
- Year rollover issues
- Loss of signal/hardware issues
 - Cabling
 - Antenna mounting
 - Installation practices

Timing Problems

- Most issues we have experienced have been in the clock firmware, not in the receiver firmware or end-device firmware
- Monitoring and logging of the IRIG-B timing output from the clock is very insightful.
 - Without monitoring IRIG-B output of clock, when time errors occur we do not know if the problem is the clock, receiver, or the end-device (PMU, DFR, etc.)
- By decoding the IRIG data stream we learned a lot. This data was previously unavailable to us due to a lack of tools.
- New IRIG monitoring tools are now on the market.

\$50 IRIG monitor



\$50 IRIG monitor

<u>Year</u>	Day	<u>Time</u>	<u>LSP</u>	<u>LS</u>	<u>DSP</u>	<u>DST</u>	<u>TZS</u>	TZ Offset	<u>TZ30M</u>	TFOM	<u>Parity</u>	<u>сто</u>	<u>SBS</u>
16	366	18:59:56	1	0	0	0	1	5	0	0	1	0	68396
16	366	18:59:57	1	0	0	0	1	5	0	0	0	0	68397
16	366	18:59:58	1	0	0	0	1	5	0	0	0	0	68398
16	366	18:59:59	1	0	0	0	1	5	0	0	1	0	68399
16	366	18:59:60	1	0	0	0	1	5	0	0	1	0	68400
16	366	19:00:00	0	0	0	0	1	5	0	0	1	0	68400
16	366	19:00:01	0	0	0	0	1	5	0	0	0	0	68401
16	366	19:00:02	0	0	0	0	1	5	0	0	0	0	68402

\$50 IRIG monitor

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16	366	18:59:60	1	0	0	0	1	5	0	0	1	0	68400
16	366	19:00:00	0	0	0	0	1	5	0	0	1	0	68400
16	366	19:00:01	0	0	0	0	1	5	0	0	0	0	68401
16	366	19:00:02	0	0	0	0	1	5	0	0	0	0	68402

Leap year, year rollover to 2017, leap second addition,

and local time zone

Impact of June 2015 Leap Second

- First real event that we monitored was the June 2015 Leap Second
 - Had no expectations of problems, monitored multiple clock firmware versions (including newest) that we have deployed in field
 - Monitored out of curiosity, and to test out the IRIG-B logger
- First sign of problem 30 seconds after Leap Second occurred
 - Main synchrophasor PDC server went to 100% CPU and 100% RAM usage
 - Stopped all PMU data flow to downstream applications

Impact of June 2015 Leap Second

- Discovered all our substation clocks had a firmware bug that performed the leap second late (5 seconds or longer)
 - Caused all PMU data to be one second later compared to actual UTC time, for as long as it took clocks to eventually perform the leap second
 - Main Synchrophasor PDC setup with a "wait time" of one second to keep PMU latency low
 - All PMU data rejected as "late" data by the PDC (data was thrown
- This did impact all substation devices that have time synchronization (DFRs, relays, etc.)
 - No operational impact to protection functions
 - Data records did have inaccurate timestamps until clocks did the leap second

Time should have progressed as:

19:59:58 19:59:59 **19:59:60** 20:00:00 20:00:01 20:00:02 20:00:03 20:00:04 20:00:05

Time actually progressed as:

Year	Day	Time	LSP	LS
2015	181	19:59:58	1	0
2015	181	19:59:59	1	0
2015	181	20:00:00	0	0
2015	181	20:00:01	0	0
2015	181	20:00:02	0	0
2015	181	20:00:03	0	0
2015	181	20:00:04	0	0
2015	181	20:00:05	0	0
2015	181	20:00:05	0	0
2015	181	20:00:06	0	0

Impacts of Timing Problems

SCADA data transfer with Comm. Processor

- Due to year roll over bug in clocks, relays set with incorrect year.
- New Comm. Processor had time checks, began filtering out SCADA traffic from relays with wrong date/time

Timing issues can impact automation

- 6th man and other polling software looking for specific timestamps.
- If records incorrectly time stamped, polling can ignore these records

New Timing Initiative

- Evaluating new clocks and technologies. Our current clock model is well over 10 years old.
- Timing has been an afterthought. Not treated with the same importance as other substation equipment. (Training)
- More rigorous testing of timing equipment:
 - We have purchased a GPS simulator to test clocks and firmware updates. (It costs less than a relay test set)
- Fundamental shift in our timing architecture:
 - Move towards better devices and architecture that ensure high accuracy and high availability of precise timing sources

Future Timing Initiatives

• New specifications:

- Backup timing source (offsite PTP, for example)
- Appropriate holdover oscillator
- Local distribution of timing consider IRIG-B alternatives such as PTP, NTP, fiber
- Remote firmware updates
- CIP compliance
- Anti-spoofing and jamming detection and handling.
- Data logging of time signals and sources.
- Firmware locks on clocks
- Thorough new firmware evaluation and testing process

Future Timing Initiatives

- We are in the process of developing a training module for the Relay technicians that covers the following topics (subject to change, this is still a work in progress)
 - Basic GPS and satellite clock theory
 - Substation timing clock usage
 - IRIG-B timing signal composition
 - Satellite clocks associated hardware
 - Installing a satellite clock
 - Programming a satellite clock
 - Installing firmware upgrade to a satellite clock

Example of tests to perform

- Daylight Saving Time On: transition from DST off to DST on
- Daylight Saving Time Off: transition from DST on to DST off
- Year Rollover UTC, no leap second: year transition at UTC midnight with no leap second
- Year Rollover UTC, leap second add: year transition at UTC midnight adding leap second
- Year Rollover UTC, leap second remove: year transition at UTC midnight removing leap second
- Year Rollover Local: year transition at local time
- June Leap Second Add: midyear leap second addition
- June Leap Second Remove: midyear leap second removal
- **GPS Epoch Week Rollover:** transition from week 1023 to week 0
- Holdover: ability of clock to retain time during loss of signal

Conclusions

- Timing in electric substations is becoming more important as more technologies and control schemes become available that rely on accurate time.
 - Need to establish a highly accurate and highly redundant/available substation timing architecture
- Treat the timing system with the same thoroughness given to other substation systems.
- Need to keep up to date with industry developments in timing and the state of GNSS.